ABSTRACT

A plurality of process liquid supply nozzles 10 are arranged at different levels on right and left sides of a semiconductor wafer W in a process bath 1. A discharge port of each of the nozzles 10 is directed toward the semiconductor In accordance with a predetermined procedure, a process liquid is discharged from one or more nozzles 10 selected from the plurality of nozzles 10. In order to perform a chemical liquid treatment, a chemical liquid is discharged from the lowermost nozzle 10, for example, and thereafter, the nozzles 10 on the upper levels sequentially discharge the chemical liquid. In order to perform a rinse liquid treatment by replacing the chemical liquid in the process bath 1 with a rinse liquid, the rinse liquid is discharged from the lowermost nozzle 10 at first, for example. Thereafter, the rinse liquid is discharged from all the nozzles 10. In this manner, efficiency and uniformity in the liquid treatment can be improved.

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